IN THE SPECIFICATION:

Please amend the specification as follows:

Please amend the paragraph beginning on Page 5, line 25, as follows:

FIGS. 4(a)-4(c) schematically illustrate the different scenarios that may occur if the IP pointer of the new extended block XB_{new} matches that of an extended block stored previously within the block cache 280 (XB_{old}). In FIG. 4(a), the older extended block XB_{old} is co-extensive with the new extended block XB_{new} but is longer. In this case, the new extended block XB_{new} should not be stored separately within the block cache 280; the older extended block may be used instead. In ~~FIG. 4(a)~~FIG. 4(b), the older extended block XB_{old} is co-extensive with a portion of the new extended XB_{new} but XB_{new} is longer. In this case, the older extended block may be extended to include the additional instructions found in XB_{new}.